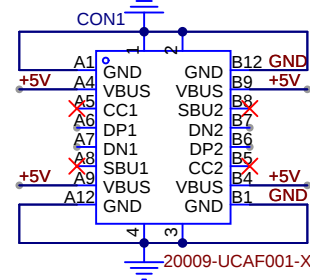
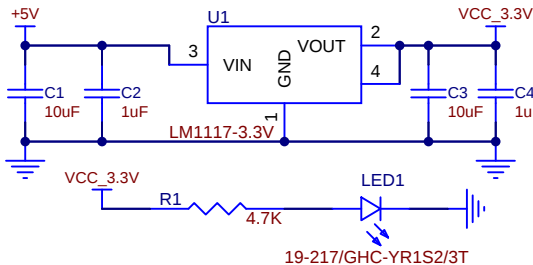


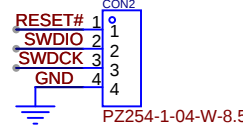
USB



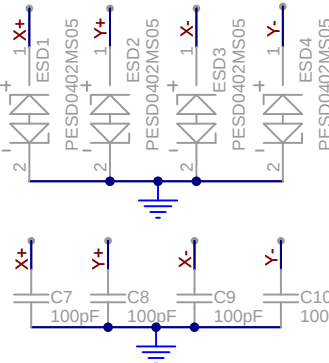
5V to 3.3V



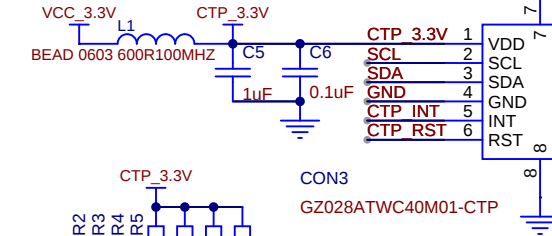
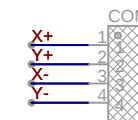
SWD



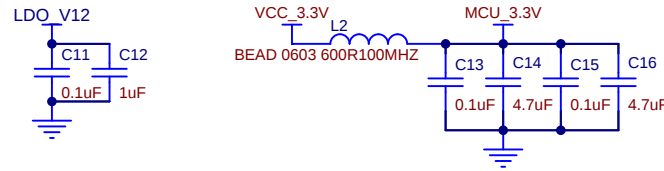
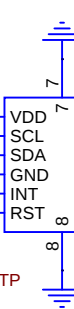
PCB必须预留接口或者测试点



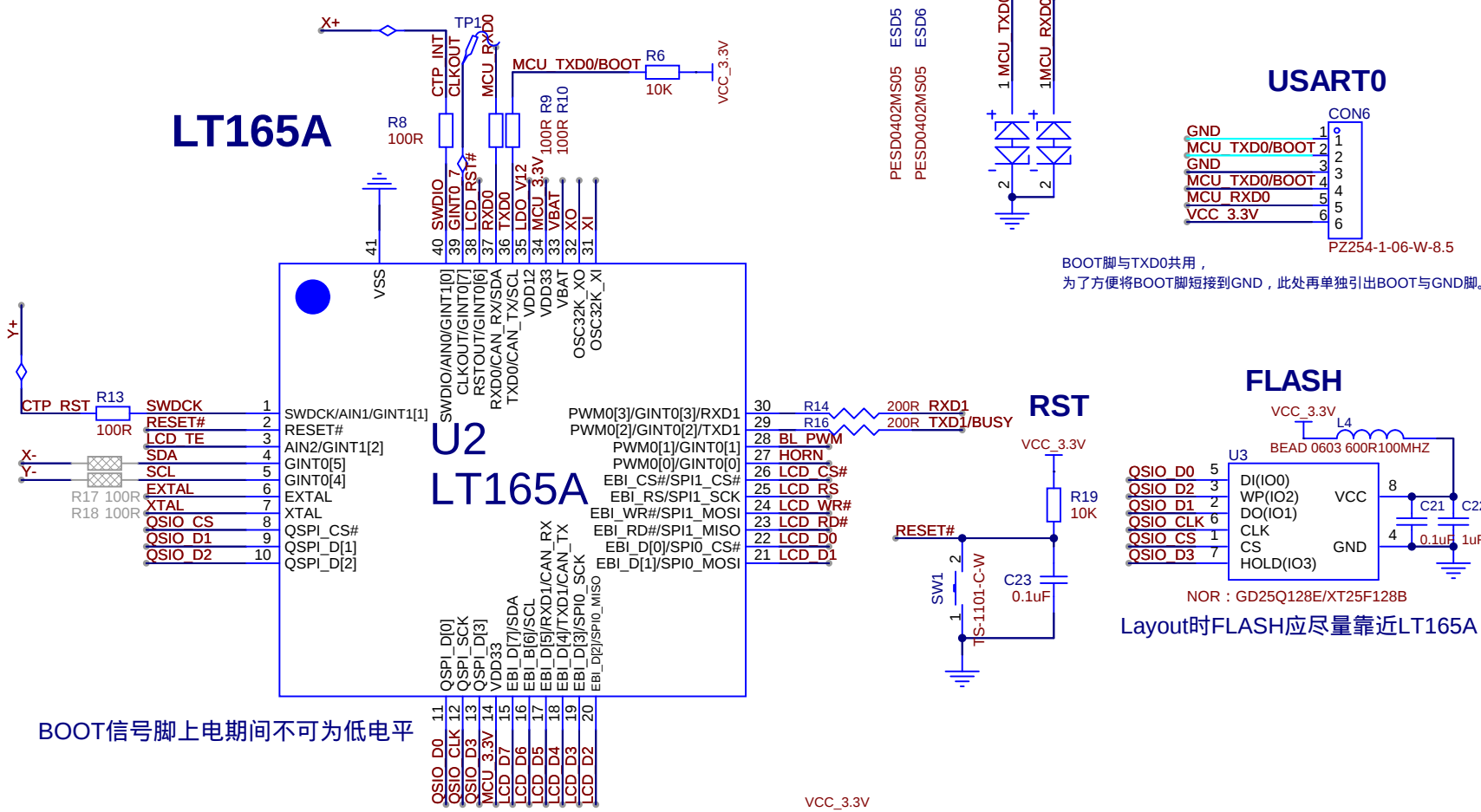
RTP(Optional)



CTP

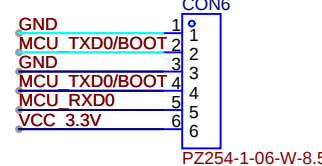


LT165A

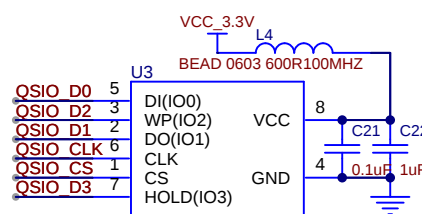


BOOT信号脚上电期间不可为低电平

USART0

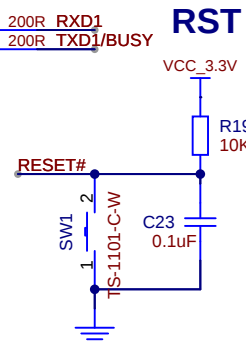
BOOT脚与TXD0共用，
为了方便将BOOT脚短接到GND，此处再单独引出BOOT与GND脚。

FLASH



Layout时FLASH应尽量靠近LT165A

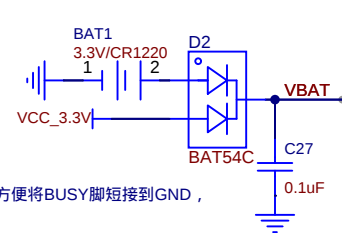
RST



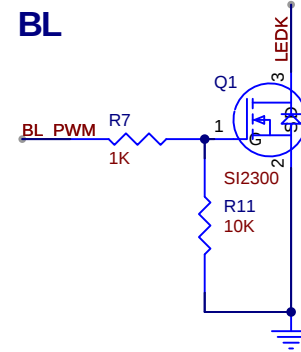
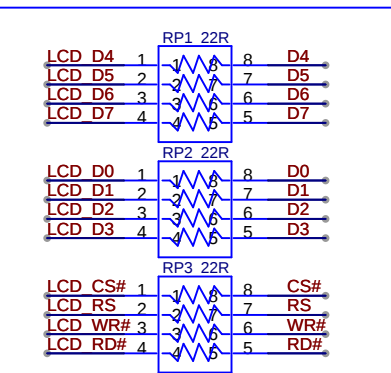
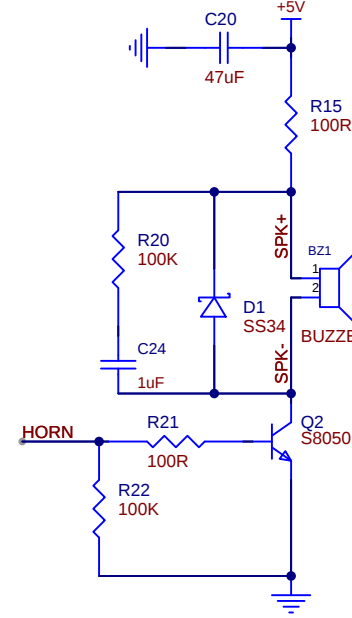
USART1



RTC



BL

蜂鸣器
BeepEMC测试可以使用50至100R磁珠
TFT的FPC需要加屏蔽

Schematic	LT165A_Demo_V1.3			Update Date	2025-09-11		
				Create Date			
Page	LT165A_Demo_V1.3			Part Number			
Drawed		乐升半导体有限公司 LeveTop Semiconductor					
Reviewed							
		VER	SIZE	PAGE	1	OF	1
		V1.3		Levetop Semiconductor.			