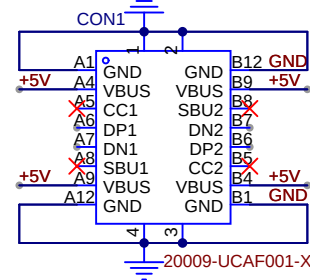
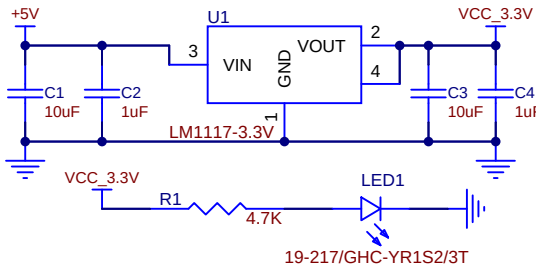


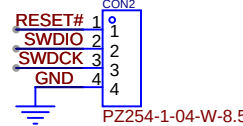
USB



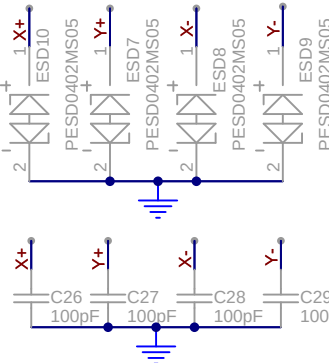
5V to 3.3V



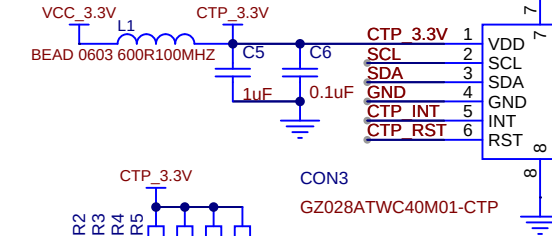
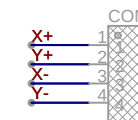
SWD



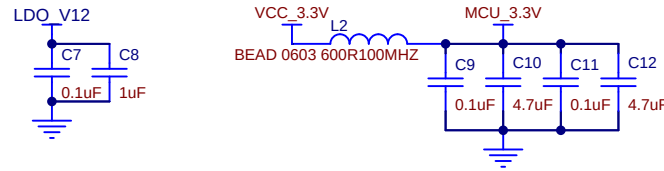
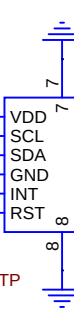
PCB必须预留接口或者测试点



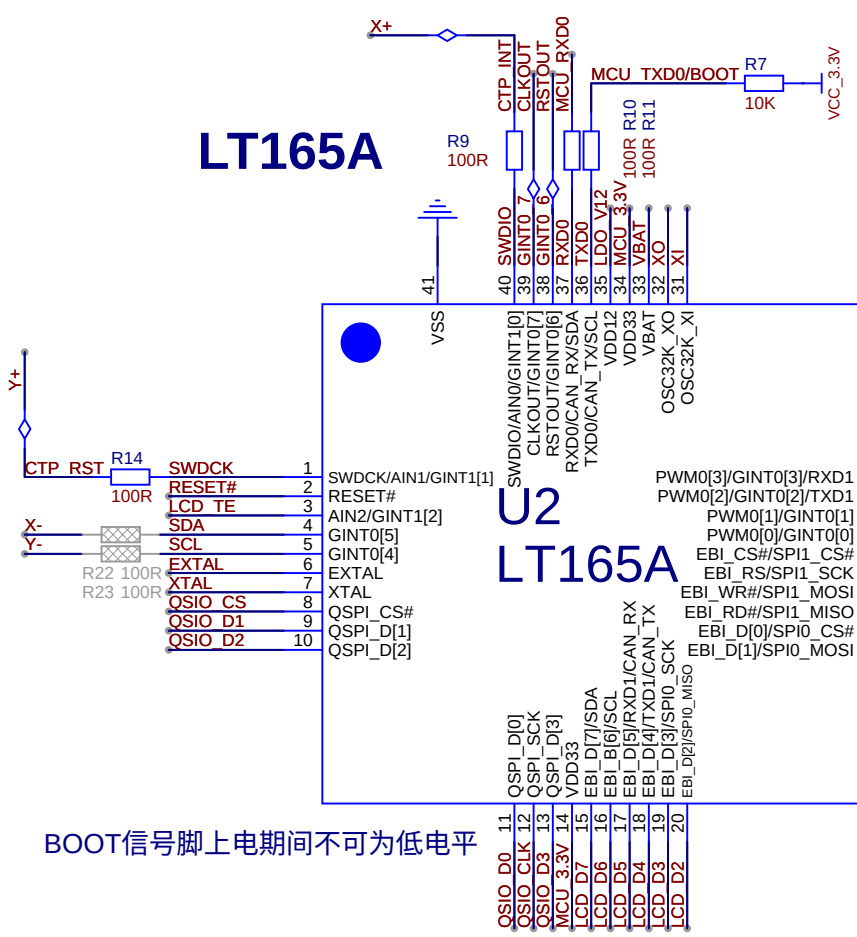
RTP(Optional)



CTP

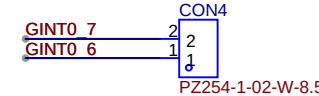


LT165A

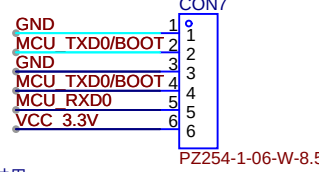
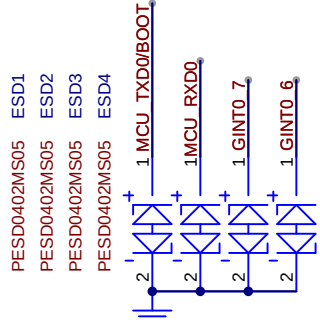


BOOT信号脚上电期间不可为低电平

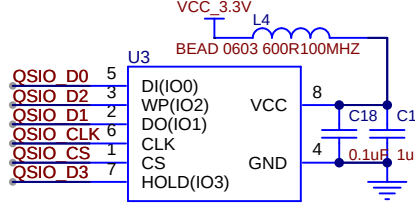
OTHER



USART0

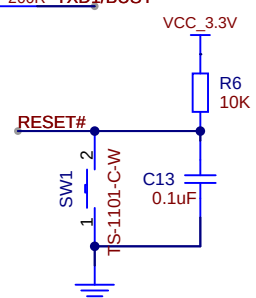
BOOT脚与TXD0共用，
为了方便将BOOT脚短接到GND，此处再单独引出BOOT与GND脚。

FLASH

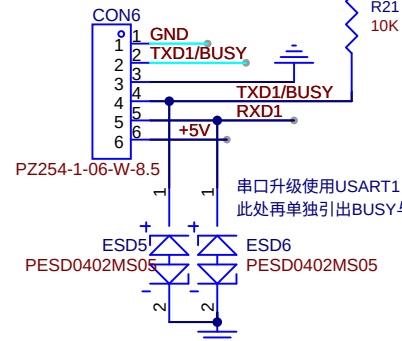


Layout时FLASH应尽量靠近LT165A

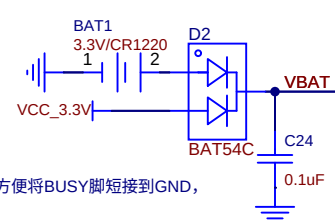
RST



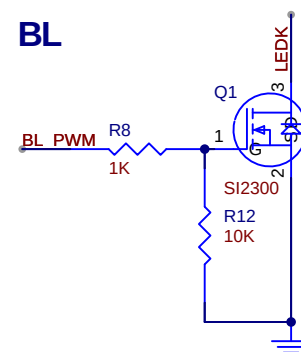
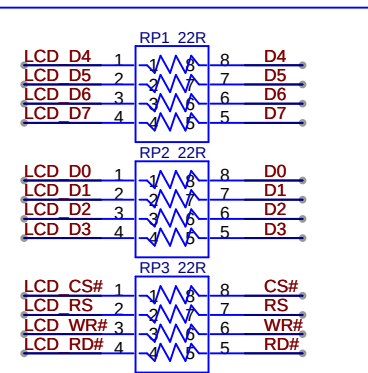
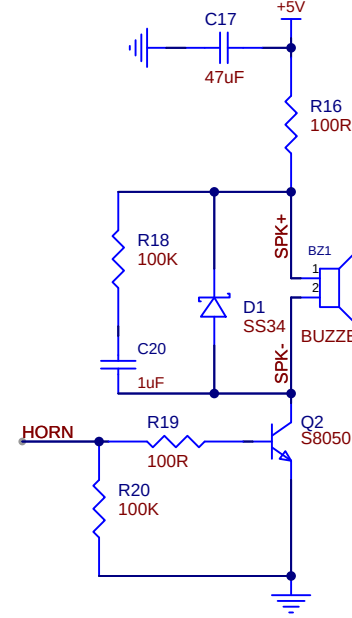
USART1

串口升级使用USART1，BUSY脚与TXD1共用，为了方便将BUSY脚短接到GND，
此处再单独引出BUSY与GND脚。

RTC



BL

蜂鸣器
BeepEMC测试可以使用50至100R磁珠
TFT的FPC需要加屏蔽

Schematic	LT165A_Demo_V1.2			Update Date	2025-07-17
				Create Date	
Page	LT165A_Demo_V1.2			Part Number	
Drawed		乐升半导体有限公司 LeveTop Semiconductor			
Reviewed					
		VER	SIZE	PAGE	1 OF 1
		V1.2		Levetop Semiconductor.	