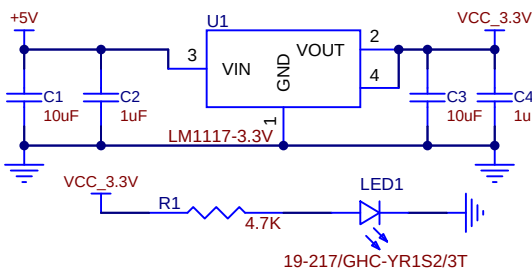
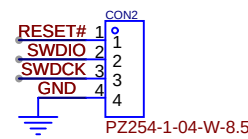


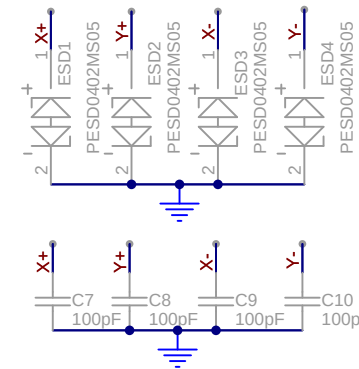
5V to 3.3V



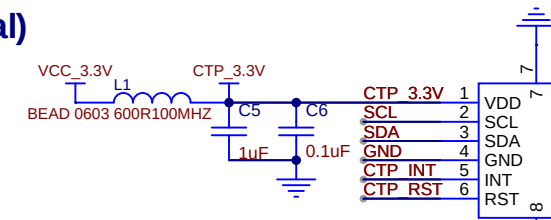
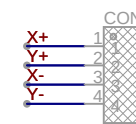
SWD



PCB必须预留接口或者测试点



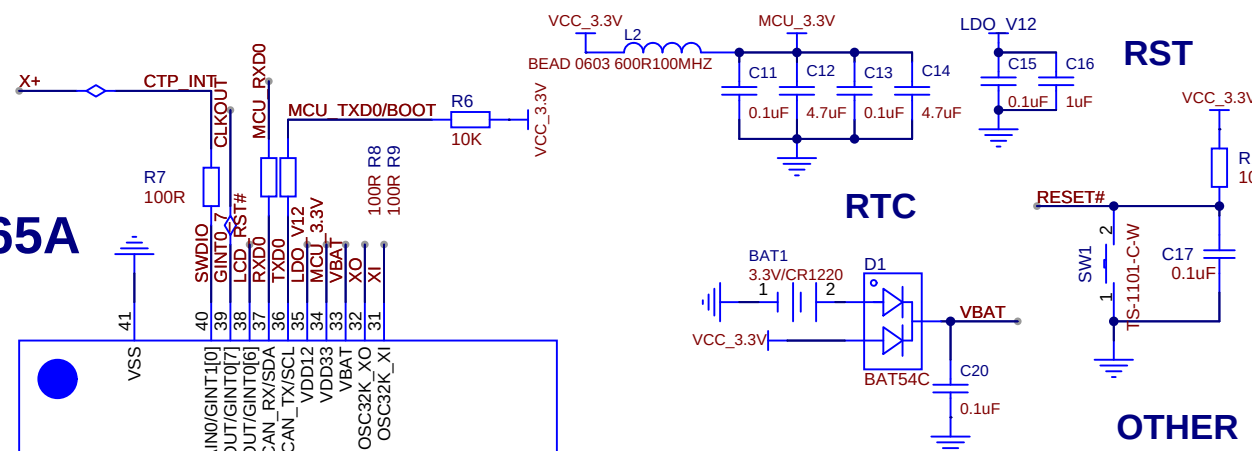
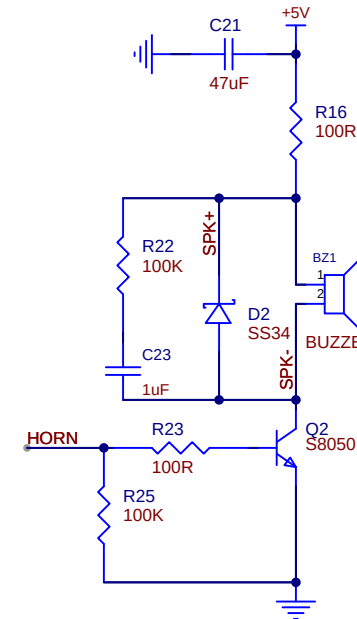
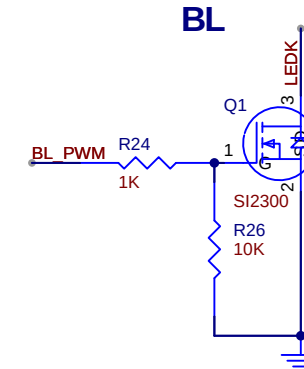
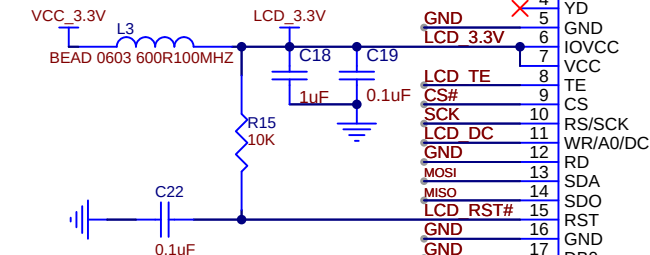
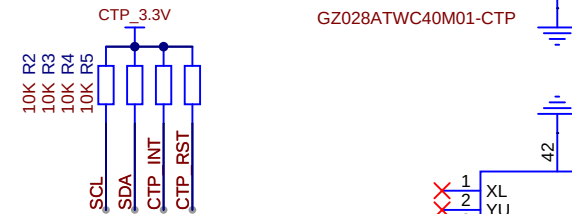
RTP(Optional)



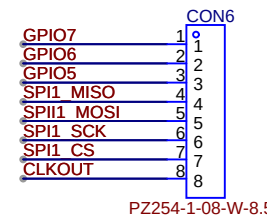
CON4

GZ028ATWC40M01-CTP

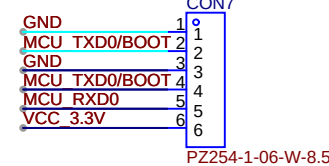
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OTHER

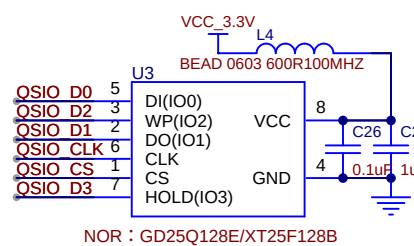


USART0



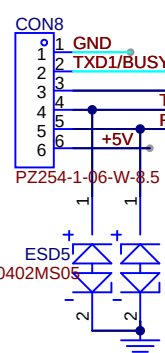
BOOT脚与TXD0共用，
为了方便将BOOT脚短接到GND，此处再单独引出BOOT与GND脚。

FLASH

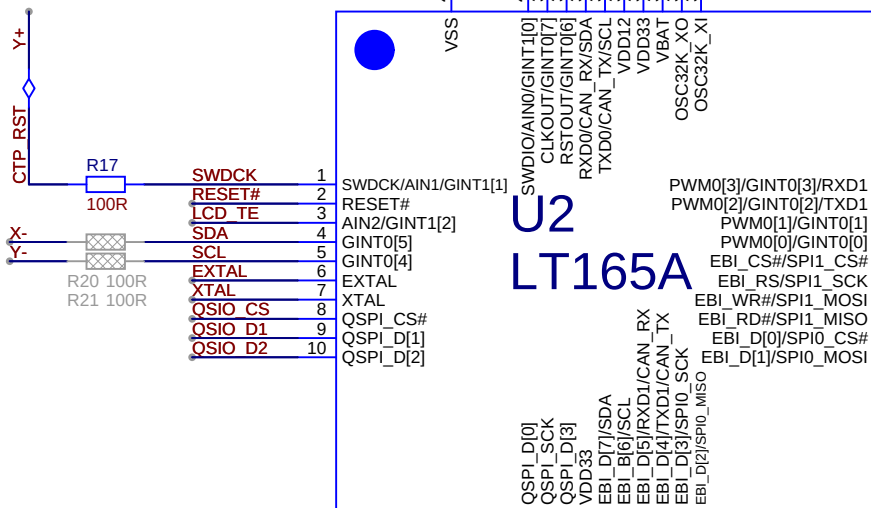


Layout时FLASH应尽量靠近LT165A

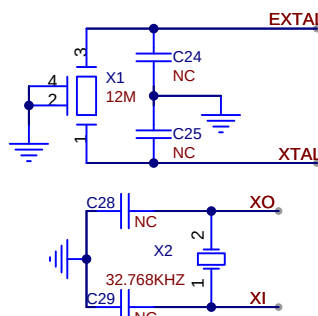
USART1



串口升级使用USART1，BUSY脚与TXD1共用，为了方便将BUSY脚短接到GND，此处再单独引出BUSY与GND脚。



BOOT信号脚上电期间不可为低电平



Schematic	LT165A_Demo_SPI_V1.1		Update Date	2025-09-11
			Create Date	2025-07-02
Page	LT165A_Demo_SPI_V1.1		Part Number	
Drawn	 乐升半导体有限公司 LeveTop Semiconductor			
Reviewed				
		VER	SIZE	PAGE 1 OF 1
		V1.1		Levetop Semiconductor.