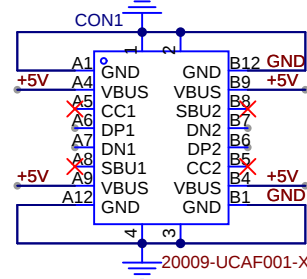
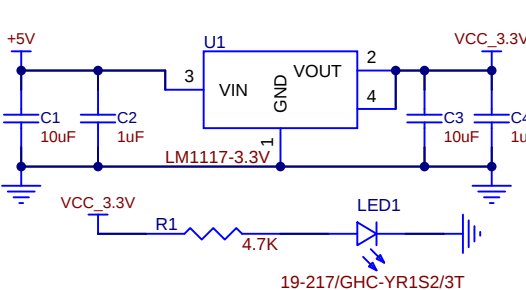


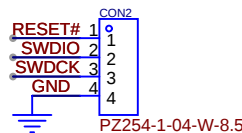
USB



5V to 3.3V

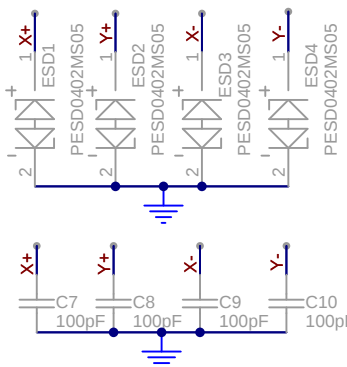


SWD

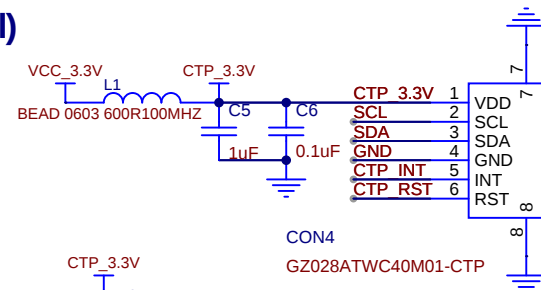


PCB必须预留接口或者测试点

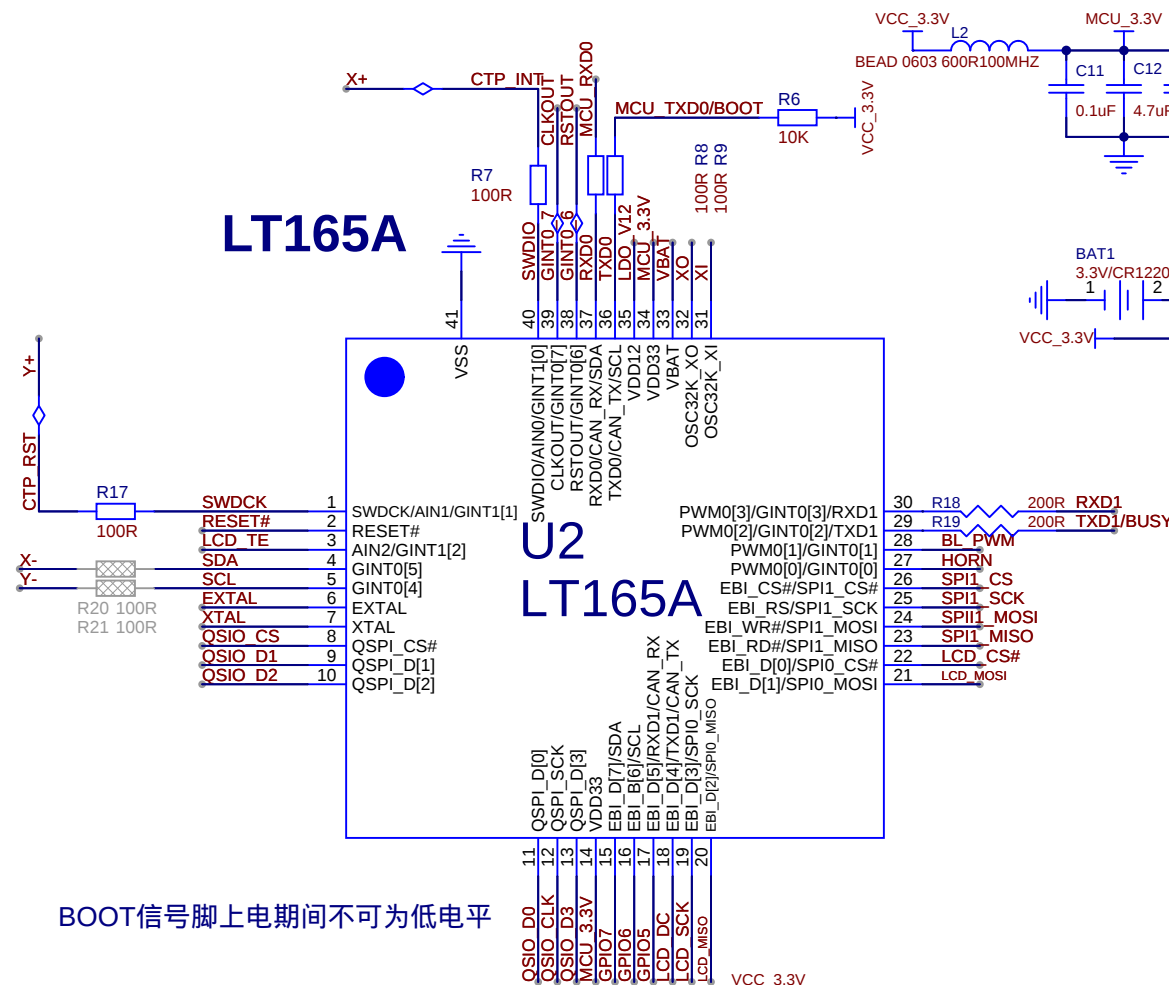
RTP(Optional)



CTP

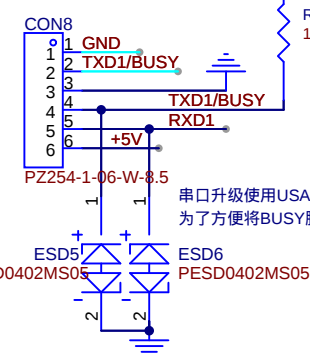


LT165A

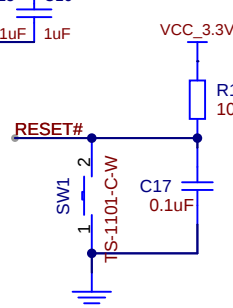


BOOT信号脚上电期间不可为低电平

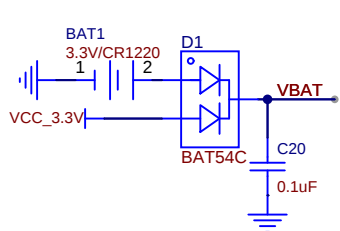
USART1



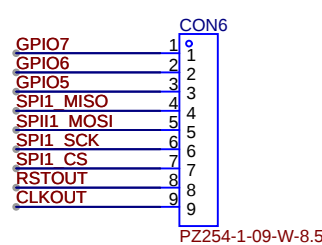
RST



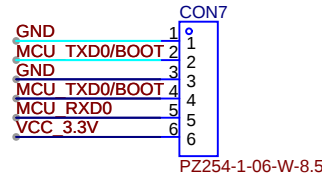
RTC



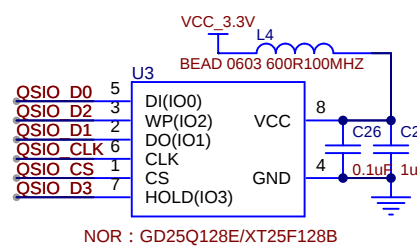
OTHER



USART0

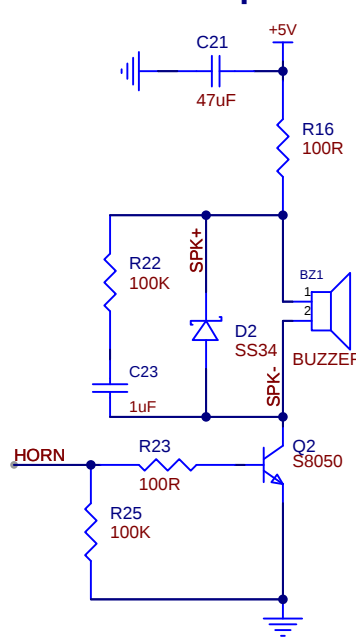


FLASH

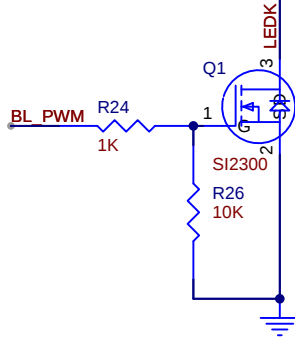


NOR : GD25Q128E/XT25F128B

Layout时FLASH应尽量靠近LT165A

蜂鸣器
Beep

BL



Schematic	LT165A_Demo_SPI_V1.0	Update Date	2025-07-17
		Create Date	2025-07-02
Page	LT165A_Demo_SPI_V1.0	Part Number	
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		VER	SIZE
		V1.0	
		PAGE	1 OF 1
		Levetop Semiconductor.	