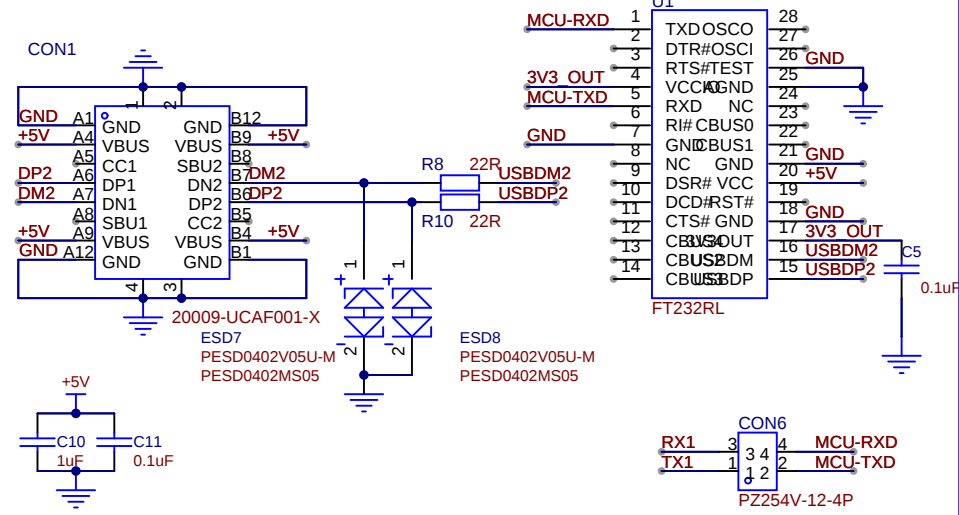
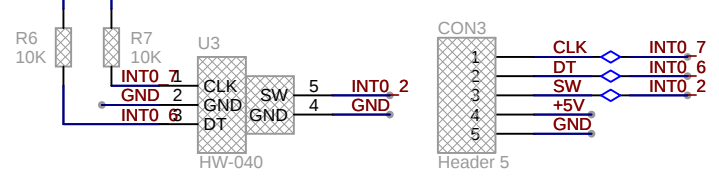


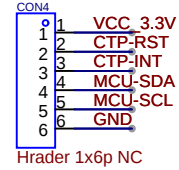
FT232RL (可选)



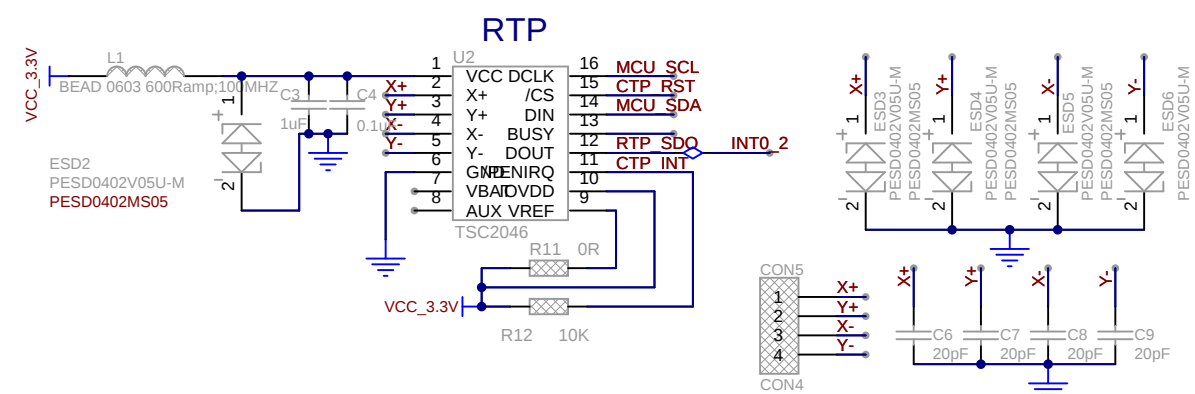
编码器 (可选)



DEBUG (可选)

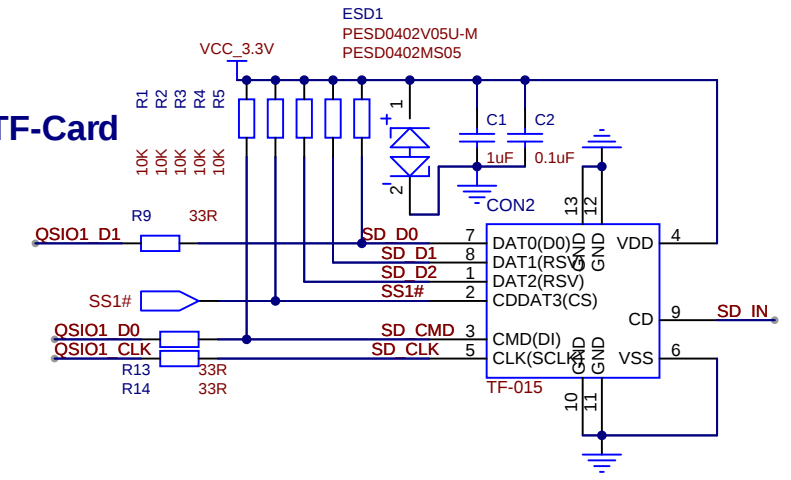


电阻触摸屏接口 (可选)

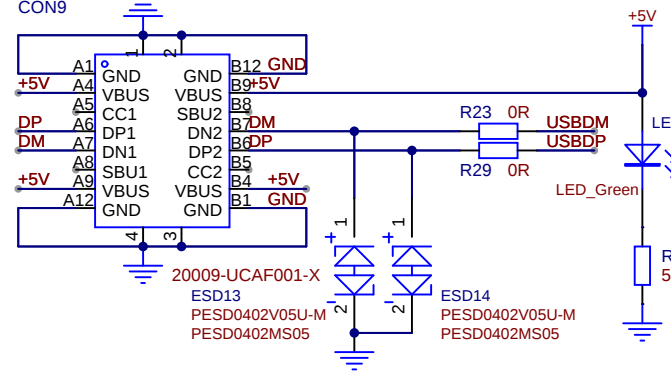


PCB上同时有CTP和RTP电路，需要在信号线上加隔离电路

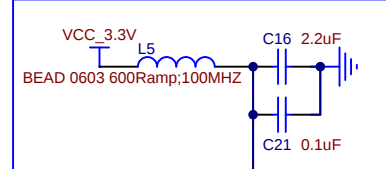
TF-Card



VCOM

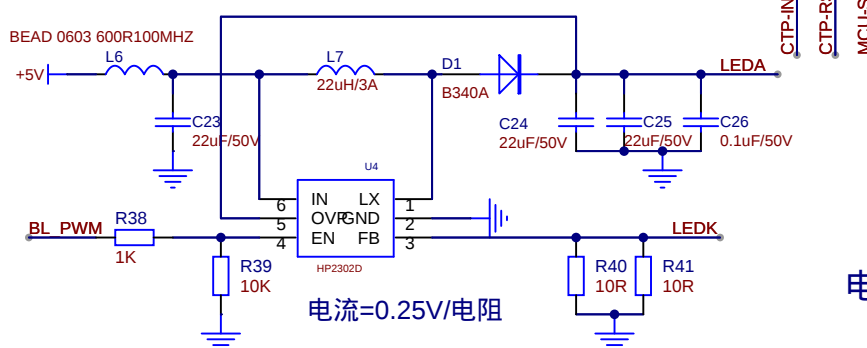


GUI_PLL供电



LCD_XI需要提供一個12Mhz晶振，可以与原有的12Mhz晶振并起来。

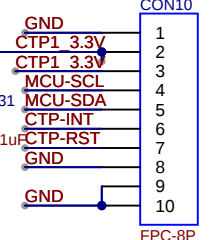
背光电路



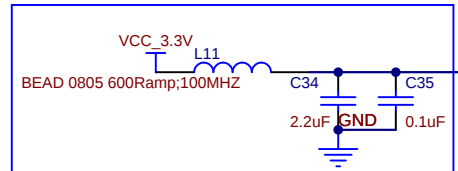
电流=0.25V/电阻

电容触摸屏接口

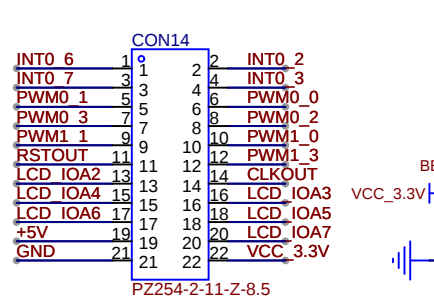
7INCH_CTP



MCU内核供电

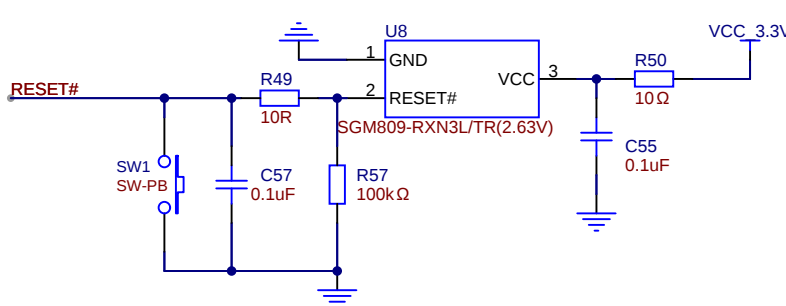


OTHER



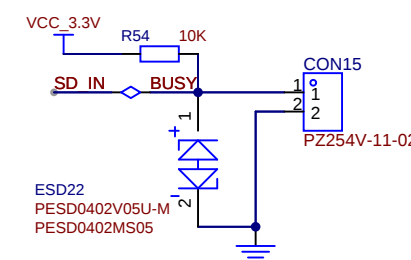
U5 LT7589B

RST电路



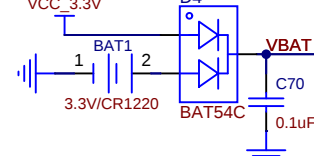
ROMBOOT(Pin48)信号脚上电期间不可为低电平，需要接上拉电阻！Pin49-Pin54只能做低速的普通IO口。

BUSY



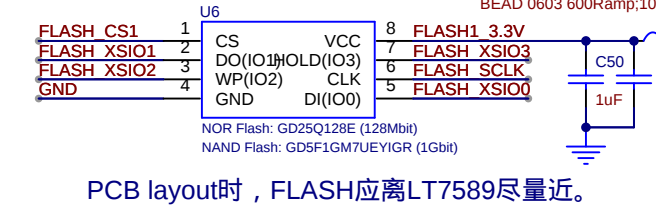
这个是烧录模式判断脚，需要上拉并引出来。

RTC



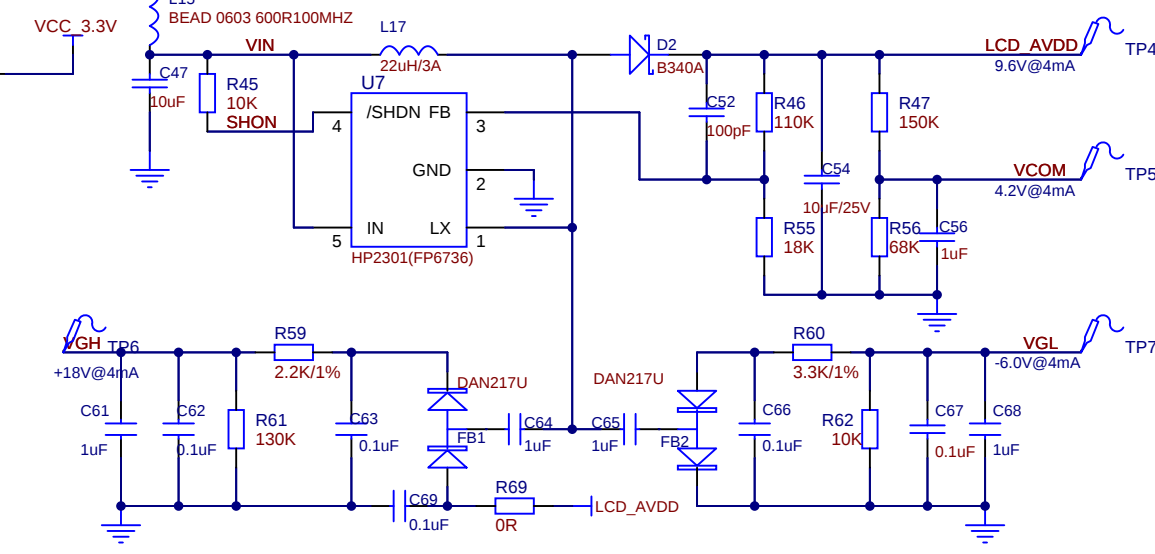
软件部分有用到计时功能，32.768K的电路需要加上

FLASH



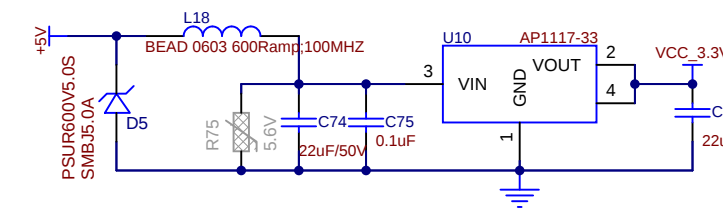
PCB layout时，FLASH应离LT7589尽量近。

7寸 VGH,VHL电路



若使用华邦的Nand Flash，FLASH_XSIO3脚则需要增加10K上拉电阻。

5V转3.3V



R75为压敏电阻，对电路有高频脉冲稳定有要求可使用

Schematic	LT7589B_Demo_V1.3	Update Date	2025-04-17
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Drawn		Part Number	
Reviewed			
	VER	SIZE	PAGE 1 OF 1
	V1.1		Levetop Semiconductor.

